

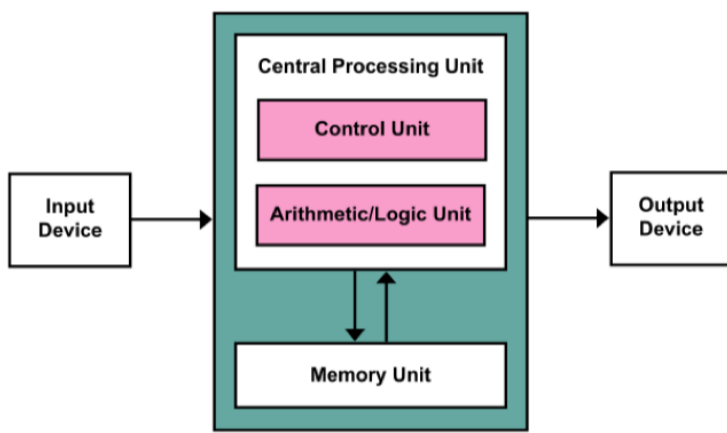
Operating systems

- Introduction:

* Features of an operating system: Design principle

- Multiprocessor } Design technique
- Multithread }
- Single/multicore → multicore is used for max^m eff.
- Centralised (Eg. resorts) → Authentication
- Distributed (Eg. satellite kitchen) → Scalability

* Neumann Architecture: (Diagram - i.)



* Operating system:

• OS (system software) is a mandatory software that enables interactions between user and the hardware.

• Software: set of instructions written in specific programming language

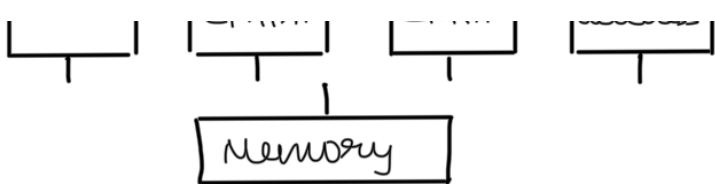
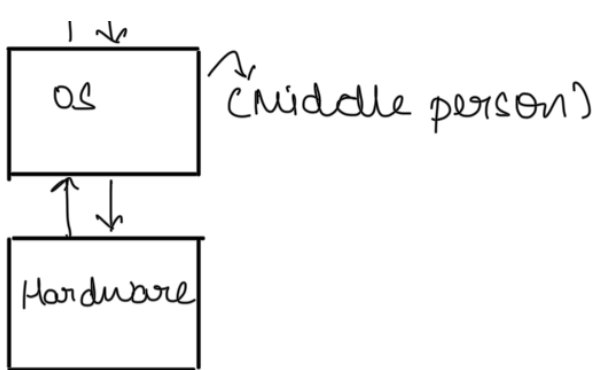
Two softwares:

Application software → dedicated code to make computer active (Eg. calculator)

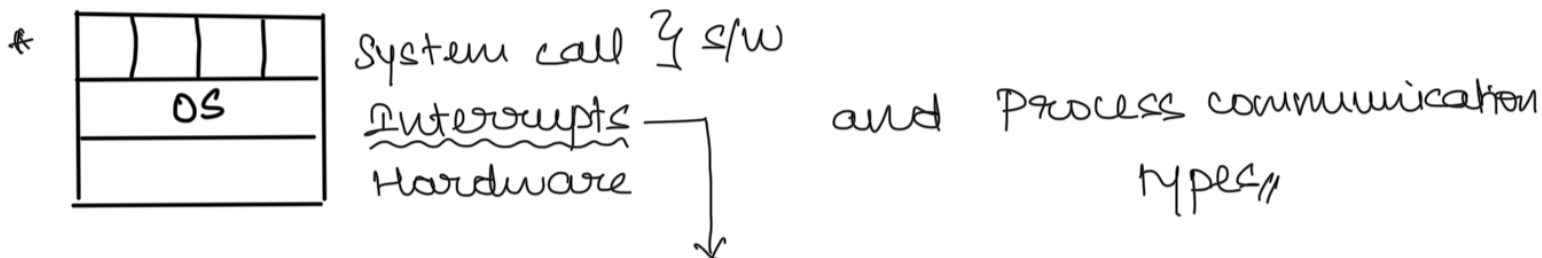
System software → mandatory to make system functional or operational.

* Important features: Availability, Integrity, Security.





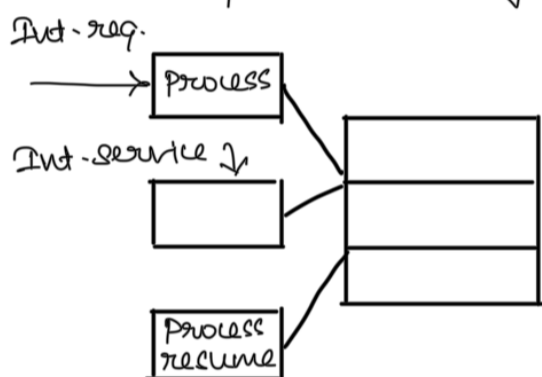
* CLI: Command user interface ; GUI: Graphic user Interface



* Control, Data & Address lines

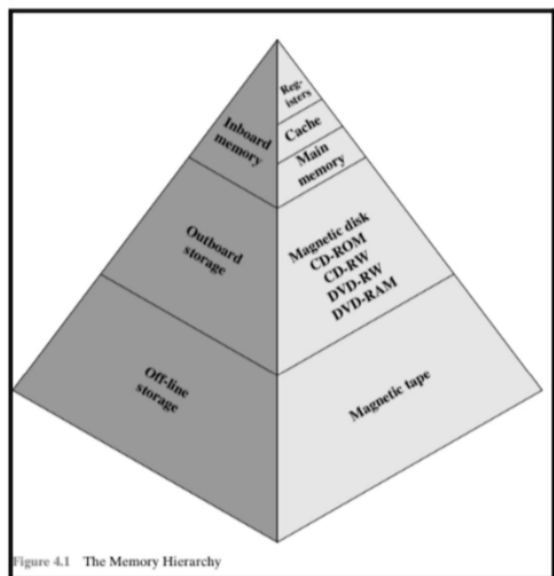
Interrupt request line and interrupt handler routine.

* Interrupt handling diagram ->



Details how the interrupts are managed (based upon relative priority)

* Memory Hierarchy:



} volatile

Difference between volatile and non volatile memory

RAM, ROM, SRAM and DRAM

Figure 4.1 The Memory Hierarchy

* Important keywords:

Memory access, process driven, interrupt driven, DMA, single process, multiprocess, time management, process management, Request, Service.

Process management, memory management, mass storage, file system, cache, I/O. → Resource manmt.

* Functions of OS:

- Resource management
 - Process management
 - Create and delete
 - Schedule threads
 - Suspend and resume
 - Process synchronization
 - Process communication
 - Memory management
 - Mode of process creation
 - Mode of execution
 - Mode of comⁿ and synⁿ
 - Mode of deployment & scalability
 - Services:
 - user interface
 - program execution
 - I/O operation
 - File system management
 - Communication
 - Error detection
- File system management
 - Mass storage management
 - Cache management
 - I/O system management
 - Security and protection
 - Visualization
 - Distributed systems
 - Display and power options
 - System view:
 - Resource allocation
 - Logging
 - Protection & security
 - Communal interpretation
 - GUI
 - System call & API
 - Process execution
 - Process sync
 - TTL - Time to live
 - Arriving time/comp.
 - Wait time

* Process: id, time and priority

Algorithms: FCFS and SJF : Non preemptive
Priority & Round Robin } : Preemptive 10M

NOTE: Linux systems are generally safe from viruses as they do not allow self execution.

FCFS: when processes have similar burst time

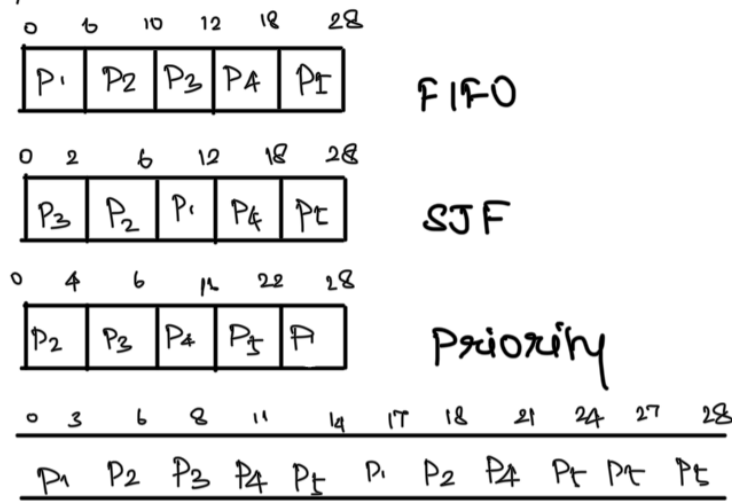
SJF: when there are many "small" tasks
↳ problem of starvation.

① The Grant Chart - 15M / 10M ↓

| Process ID | Burst Time | Priority |
|------------|------------|----------|
| P1 | 6 | 5 |
| P2 | 4 | 1 |
| P3 | 2 | 2 |
| P4 | 6 | 4 |
| P5 | 10 | 3 |

If no priority is mentioned, assume equal priority
 (as in round robin)

Gantt chart:



Note:
 If priority and burst time are same, "oldest" process is considered
 (It has less T+1 (Time to live))

| | | | | |
|----------------|----------------|----------------|----------------|----------------|
| | R ₁ | R ₂ | R ₃ | R ₄ |
| P ₁ | 3-3 | 3-0 | - | - |
| P ₂ | 3-1 | 1-0 | - | - |
| P ₃ | 2-0 | - | - | - |
| P ₄ | 3-3 | 3-0 | - | - |
| P ₅ | 3-7 | 3-4 | 3-1 | 1-0 |

| | | |
|------------------|-----------|----------|
| | W | E |
| P ₁ : | 14-3=11 | 17-0=17 |
| P ₂ : | 3+17-6=14 | 18-3=15 |
| P ₃ : | 6 | 8-6=2 |
| P ₄ : | 8+7=15 | 21-8=13 |
| P ₅ : | 11+10=21 | 28-11=17 |

| | $\frac{W}{E}$ | P ₁ | P ₂ | P ₃ | P ₄ | P ₅ |
|----------|---------------|----------------|----------------|----------------|----------------|----------------|
| FIFO | | 0 | 6 | 10 | 12 | 18 |
| | | 6 | 10 | 12 | 18 | 28 |
| SJF | | 6 | 2 | 0 | 12 | 18 |
| | | 12 | 6 | 2 | 18 | 28 |
| Priority | | 22 | 0 | 4 | 6 | 16 |
| | | 28 | 4 | 6 | 16 | 22 |
| RR | | 11 | 14 | 6 | 15 | 21 |
| | | 17 | 15 | 2 | 13 | 17 |

- (i) FIFO:
 AWT: 9.2
 AET: 14.8
- (ii) SJF:
 AWT: 7.6
 AET: 13.2
- (iii) Priority:
 AWT: 9.6
 AET: 15.2
- (iv) RR:
 AWT: 13.4
 AET: 12.8

② consider the following process:
 process Burst Arrival

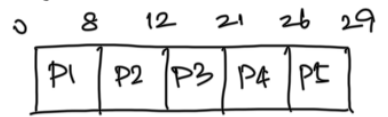
| | | |
|----------------|---|---|
| P ₁ | 8 | 0 |
| P ₂ | 4 | 1 |
| P ₃ | 9 | 2 |
| P ₄ | 5 | 3 |
| P ₅ | 3 | 4 |

calculate wait time and completion time for
 (i) SJF
 (ii) FCFS
 (iii) RR → Quanta → 2.

(i) FCFS

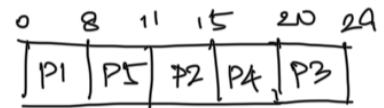
| | A | B | S | W | TA | ($W = S - A$) ($TA = W + B$) |
|----|---|---|----|----|----|-------------------------------------|
| P1 | 0 | 8 | 0 | 0 | 8 | $W = 8$ |
| P2 | 1 | 4 | 8 | 7 | 11 | $AW = 11.4$ |
| P3 | 2 | 9 | 12 | 10 | 19 | $TA = 86$ |
| P4 | 3 | 5 | 21 | 18 | 23 | $ATA = 17.2$ |
| P5 | 4 | 3 | 26 | 22 | 25 | |

Gantt:



(ii) SJF:

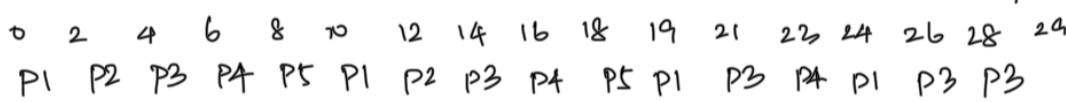
| | A | B | S | W | TA | $W = 44$ $AW = 8.8$ $TA = 73$ $ATA = 14.6$ |
|----|---|---|----|----|----|---|
| P1 | 0 | 8 | 0 | 0 | 8 | |
| P5 | 4 | 3 | 8 | 4 | 7 | |
| P2 | 1 | 4 | 11 | 10 | 14 | |
| P4 | 3 | 5 | 15 | 12 | 17 | |
| P3 | 2 | 9 | 20 | 18 | 27 | |



(iii) Round Robin \rightarrow Quantum = 2

| | R1 | R2 | R3 | P4 | P5 |
|----|-----|------------|------------|-----|------------|
| P1 | 2-6 | 2-4 | 2-2 | 2-0 | - |
| P2 | 2-2 | 2-0 | - | - | - |
| P3 | 2-7 | 2-5 | 2-3 | 2-1 | <u>1-0</u> |
| P4 | 2-3 | 2-1 | <u>1-0</u> | - | - |
| P5 | 2-1 | <u>1-0</u> | - | - | - |

\rightarrow Use ready Queue



Questions:

1. What does OS do
2. User vs. system
3. modules of resource mgt.
4. sys process commⁿ
5. Hierarchy, multithread vs. multiprocessor.

Operating system services:

- User interface
- Program execution
- I/O operations

- File system manipulation
- Communication
- Error detection
- Resource allocation and logging

User interface:

CUI and GUI, Touchscreen.

System calls:

File copy:

get input name
 write prompt to screen
 accept input
 Acquire target name
 Accept input

CR and WJ execute too

Open file in 'R' mode:

- if file does not exist \Rightarrow exit

Open target file 'W' mode:

- if file exists, abort loop
- Read from input file, 10 MB
- until read, fail
- close O/P file
- Work completion message in screen
- Terminate process

Application Programming Interface (API):

- create process
- Instances (a.txt)

- RTE: Run Time Environment

Types of system calls:

- Process control: create and terminate process
load and execute
get process attributes
wait and signal
allocate and free memory

File management:

- Create and delete
- open and close
- Read, write, reposition
- get and set file attributes

Device Management:

- Request and release
- Read, write, reposition
- get and set dev. attributes
- Attach and detach logical device.

Information Maintenance:

- get and set time and system data
- get and set device and process attributes
- shared message and message priority
- communication
- create and schedule messages, communication
- send and receive.

- transfer state is formatted
- attach and detach logical device
↳ reusable

System services:

- state in function
- file management
- file modification
- program language support
- program load and execution.

Protection:

- get and set file and device permission.

Communication and background services:

- demon application program

Note:

9-bit protection mechanism:

Here, $r = 4$, $w = 2$, $e = 1$

↳ $2 + 1 = 3 \Rightarrow$ write + execute

| | | | |
|----|---|---|---|
| | O | G | E |
| rw | r | r | r |
| 7 | 4 | 4 | |

MOST COMMON: 741

31/8/24

1) Types of system calls

- Process Control
- File management
- Device management
- Information maintenance
- Communication
- Protection

2) System services:

- File management
- Status information
- File mediation
- Programming language support
- Program loading & execution
- Communication

Background services - App program & Daemons

(i) Linker

Absolute

(ii) Loader

Relocatable

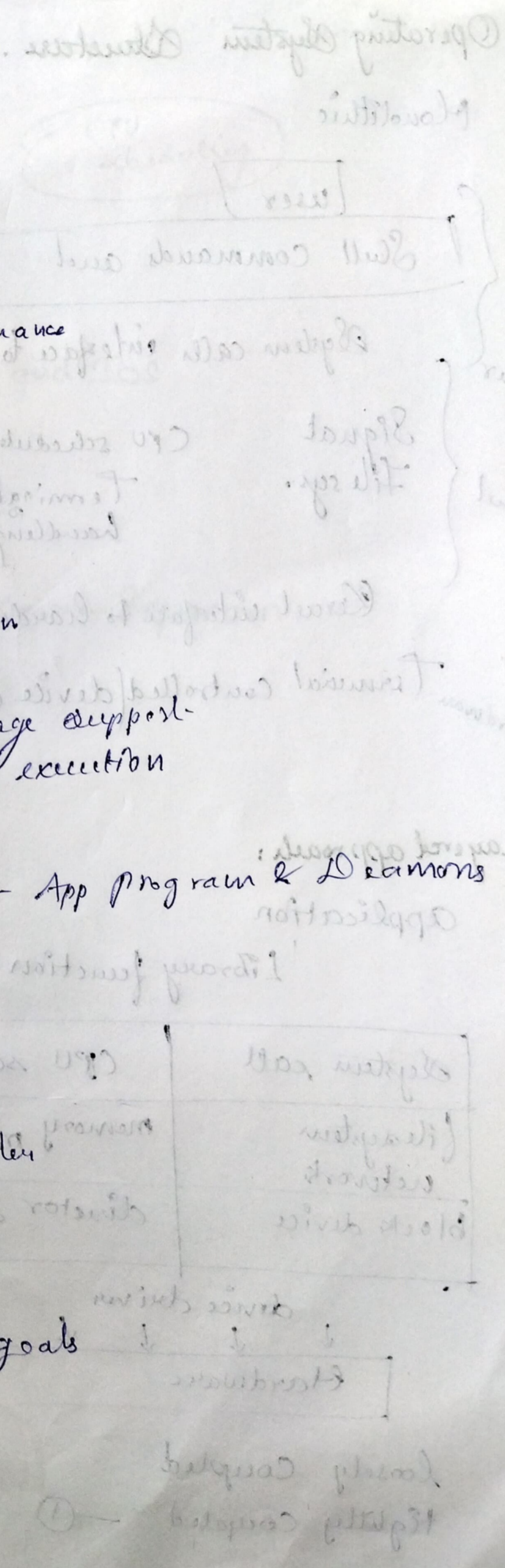
(iii) Assembler

(iv) Translator (Interpreter)

(v) Compiler

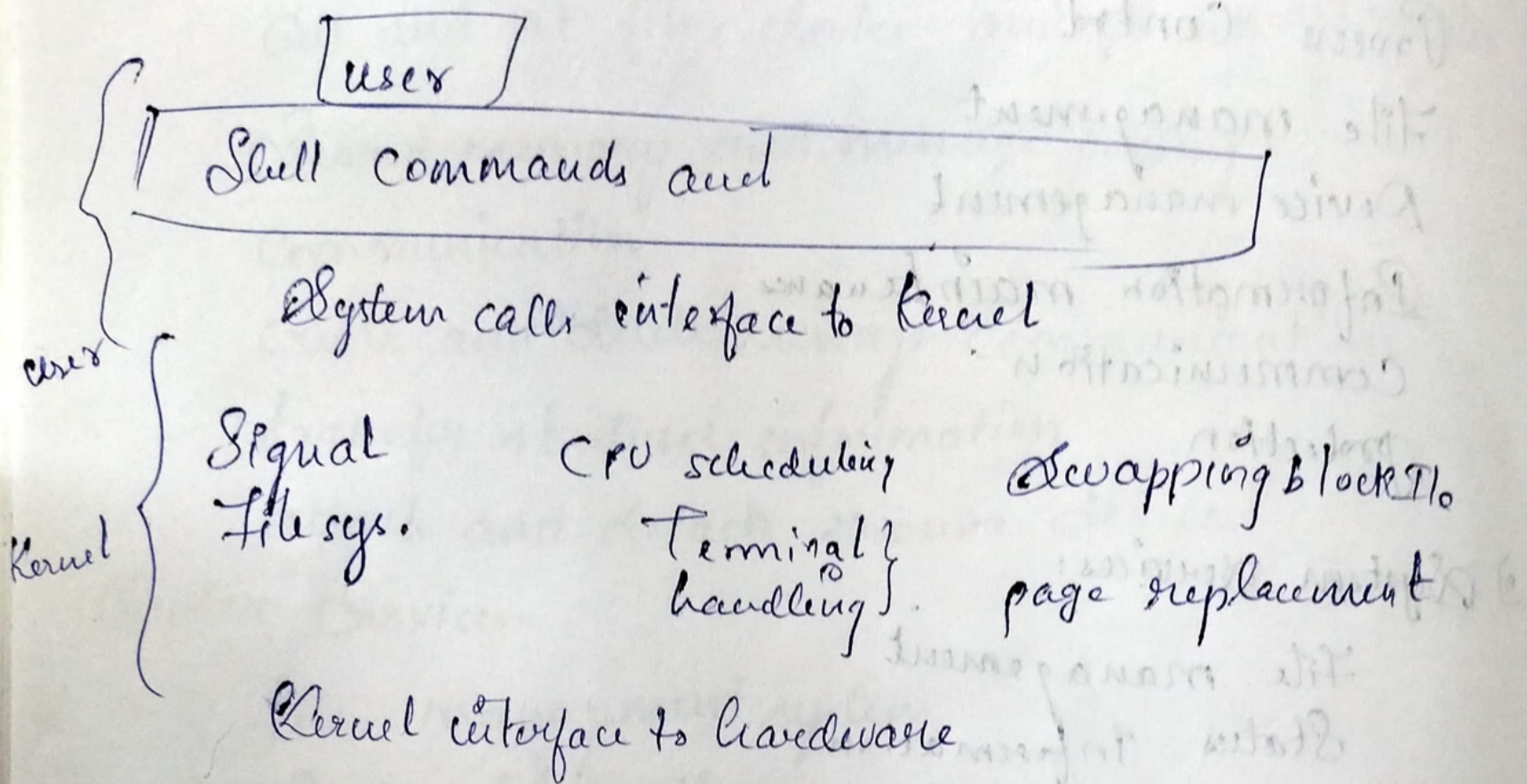
OS Design Goals

user goals and system goals



Operating System Structure.

Monolithic



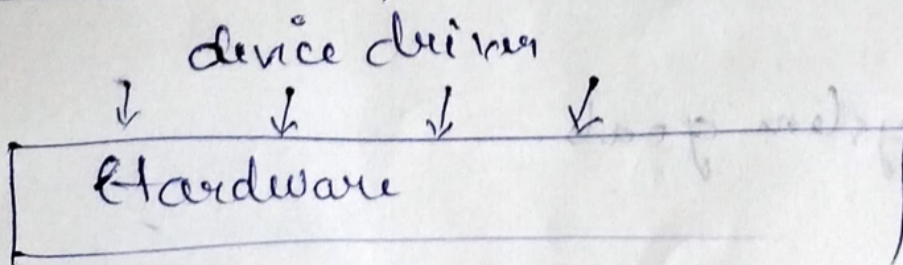
Hardware Terminal controlled / device controller / Memory controller

Layered approach:

Application

Library function

| | |
|-----------------------|------------------|
| System call | CPU scheduling |
| filesystem network | memory manager |
| block device | director devices |



loosely coupled
tightly coupled

①

Micro Kernels.

Interpreter

memory

CPU
scheduling

modules — Key

Loadable Kernel modules

Hybrid system — Mac OS and IOS

User experience layer

Application processor,

Core framework Kernel

Android

Kernel abstraction and Kernel Expansion

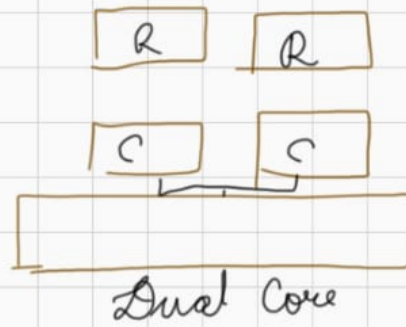
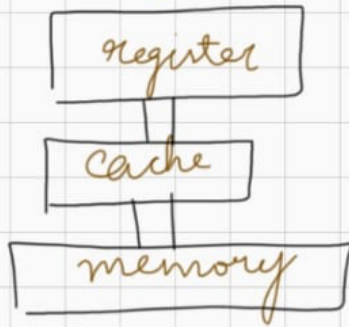
API and

Android Runtime — ART

Boot start

Date: 6/9/24

Single Core and multi core

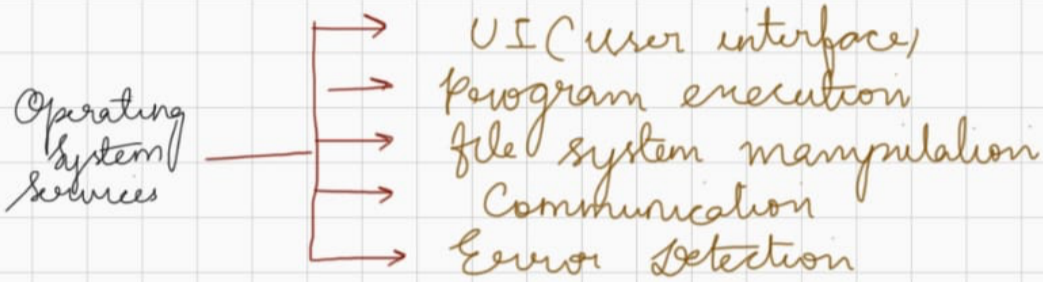


Operating System

- user view
- system view
- multiprocess
- multiprogram

| | |
|-------|-------|
| Job 1 | — |
| Job 2 | Job 2 |
| Job 3 | Job 4 |
| Job 4 | Job 3 |
| | Job 5 |

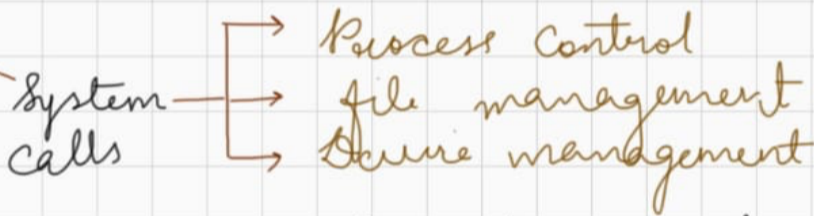
- Time sharing hardware
- swapping
- logical and physical memory
- system → user
- ↓ kernel



User operation

- command interpreter
- graphical user interface

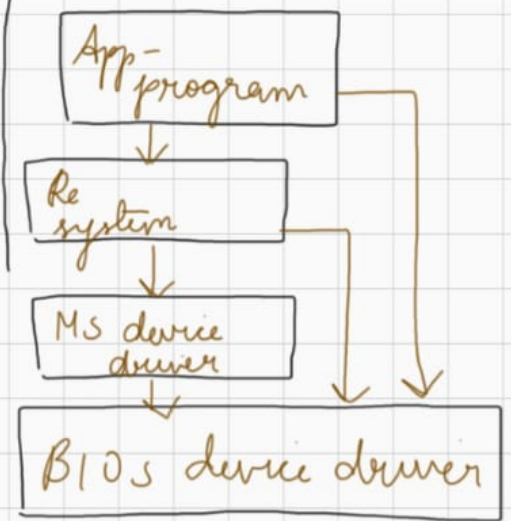
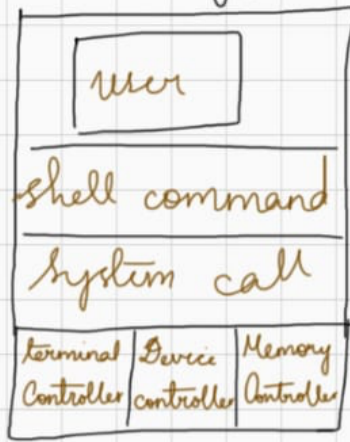
Communication



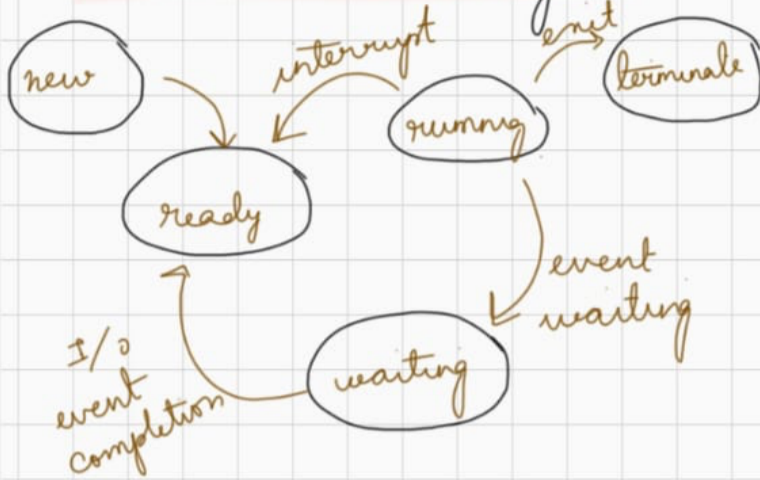
Information

- ★ Design goals
- ★ Mechanism and policies
- ★ OS structure

layered approach

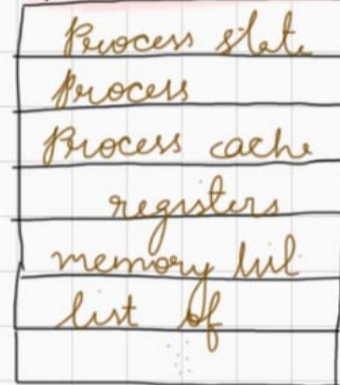


Process state Diagram



- New
- Running
- Waiting
- Ready
- Terminated

PCB - Process control block

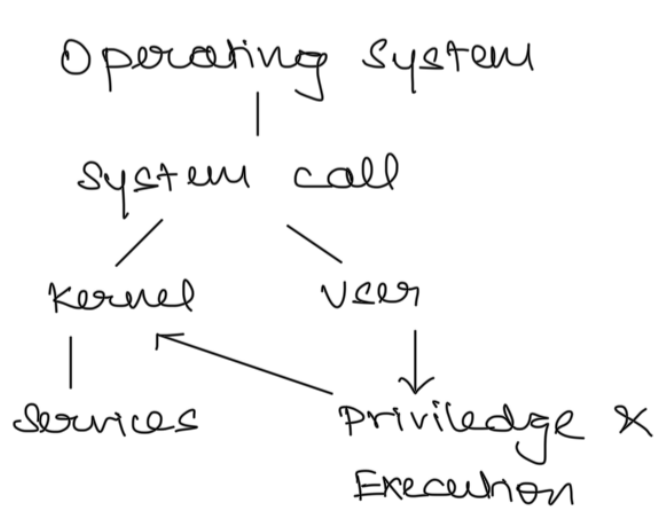
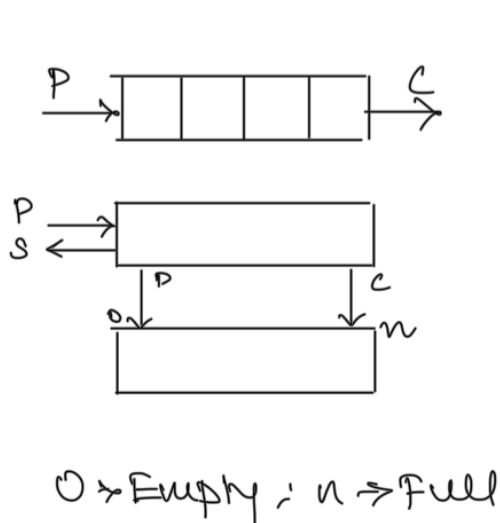


- ★ Process state
- ★ Program control
- ★ CPU registers
- ★ CPU selection info
- ★ mem management
- ★ account information
- ★ I/O state information

- thread
- scheduling queue
- FCFS, SJF, PS, RR - scheduler
- PID and PWD
- long time, short time job scheduler, cpu
- I/O bound process, CPU bound process
- mid scheduler / swapping scheduler
- content switching
- process termination

Operating System:

- system design and trade off
- Process model
- scheduler organisation
- Process scheduling
- Pre-emptive and non-preemptive
- Scheduling Algorithm: FCFS, SJF, Priority, RR
- Process co-operation
- Inter process communication (IPC)
- Process synchronization
- Synchronization issues.
- Critical section
- Mutual exclusion
- Producer-Consumer
- Dining Philosopher Problem
- Array \rightarrow produce, consume
- Race condition, semaphores



Semaphores

- Semaphore proposed by Edsger Dijkstra, is a technique to manage concurrent processes by using a simple integer value, which is known as a semaphore.

- Semaphore is simply a variable which is non-negative and shared between threads. This variable is used to solve the critical section problem and to achieve process synchronization in the multiprocessing environment.

Helps in IPC

- A semaphore S is an integer variable that, apart from initialization, is accessed only through two standard atomic operations: `wait()` and `signal()`.

`wait()` → P [from the Dutch word **proberen**, which means "to test"]

`signal()` → V [from the Dutch word **verhogen**, which means "to increment"]

* Definition of `wait()`

Tests if any other process is using a resource

P (semaphore S)

{

while ($S \leq 0$);

$S--$;

}

* Definition of `signal()`

V (semaphore S)

{

$S++$; // Basically, resource is available @ this pt.

}

Note: Semaphore is common betⁿ processes.

All the modifications to the integer value of the semaphore in the `wait()` and `signal()` operations must be executed indivisibly. That is, when one process modifies the semaphore value, no other process can simultaneously modify that same semaphore value.

Types:

(1) Binary / Mutex Semaphores (Mutual Exclusion),

0 → process has to wait

1 → process can access resource

① Initial value is 1 → process thro' while → changes to 0

② Another process comes up → here S is 0 →

process is stuck in while loop \rightarrow "waits".

③ Upon exiting, first process calls signal fn.
 $\rightarrow S=1$ @ this pt.

④ The 2nd process will break loop and execute now

(ii) Counting Semaphore:

- Unrestricted domain
- ctrl. access to a resource that has multiple instances

① say, a resource can be accessed by 2 resources.
 $\rightarrow S=2$

② First process enters $\Rightarrow S=1$

③ Second process enters $\Rightarrow S=0$

\downarrow

when 2nd one enters, it has to wait.

- Process control blocks

\downarrow

Process state

Program counter

CPU Register

CPU scheduling information

Memory management information

Accounting info

I/O status

- Threads
- I/O Bound Process
- CPU Bound Process
- Scheduling Queue
- CPU scheduler
- context switching
- Hardware support
- Process creation
- Fork and Pipeline

Process \rightarrow coreaction, term.
Cascade termination
Zombie process
FDFS & SJF.

* Service

|

Empty \rightarrow Process \rightarrow visible

terminated

ready queue

IPC and Information sharing
Computational speedup
Modularity
Shared memory
Merge passing

send, receive
synchronisation
blocking send
non-blocking send
Block/N-B receive

* Buffering:

Pipes - parent-child
↳ Generate and name pipes.

Client server communication, socket and server socket

RPC: Remote procedure calls

* Multiple programming → data and task parallelism

Thread model: one to one, many to one
many to many.

* CPU Scheduling Algorithms:

↳ can be pre-emptive / non-preemptive

Infinite wait → starvation and ageing of process.

* Other methods:

- multilevel queue scheduling
- Realtime process
- system process
- interactive & batch process
- multilevel feedback

* Load Balancing scheduling

Push and pull migration
Process affinity: soft and hard

* Process synchronisation

Multiple-process, limited resource
critical section process
mutual exclusion, bounded waiting.

* Hardware support: strong / weak
Priority inversion method

Process scheduling → Memory access → Memory
available → Request / Register.

* Addressing Binding

Compile time

Local time

Execution time

logical, physical and virtual.

Memory allocation, dynamic allocation,
Best / first / worst fit.

Internal / External factors

Date: 19/9/24

Process synchronization
 Process and resources
 I/O + memory
 sender/receiver
 Buffer size

The critical section
 Mutual exclusion
 Progress
 Bounded wait
 Pre-emptive kernel and
 non-pre-emptive kernel
 Peterson Solution
 memory barrier

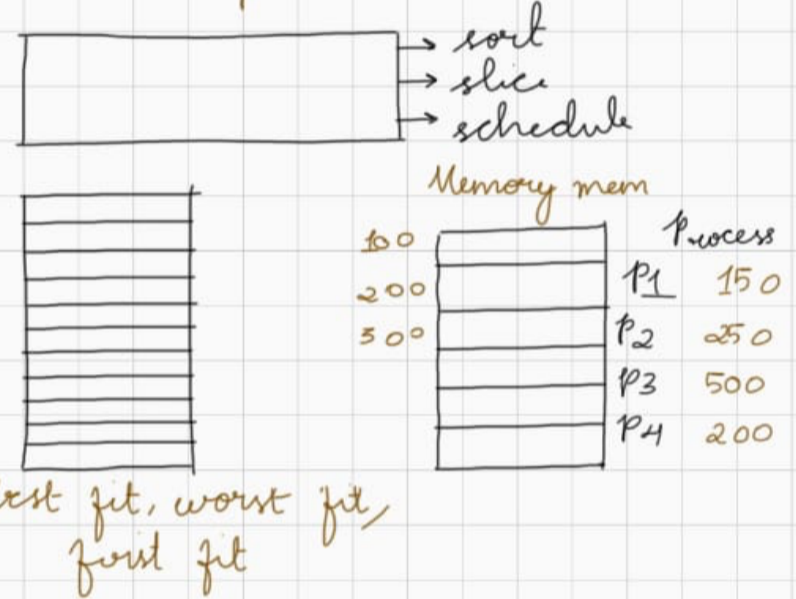
Mutex locks
 monitor usage
 signal and wait
 signal and continue
 Infinite wait (deadlock)
 Synchronisation Problems
 - bounded buffer problem
 - Reader writer problem

Dining Philosopher problem
 Concurrency control problem
 Deadlock handling
 Main memory

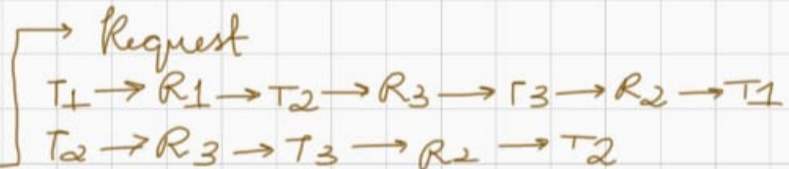


Address binding
 logical vs Physical vs Virtual
 Dynamic loading
 Dynamic linking and shared library
 load, link, compile, execute

Contiguous memory allocation
 Memory Reallocation
 Variable particular



Request → use →
 Deadlock condition
 Mutual exclusive
 hold and wait
 No. of exemption
 Circular wait
 Resource allocation graph



R **P**

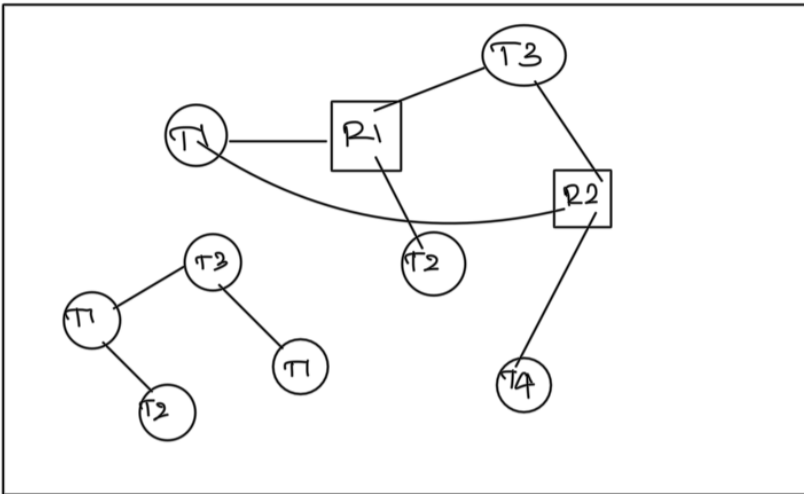
E. T1 → R1, T2 → R3, R-T2, R2-T2, R3-T3

MAC - Machine Authentication code
 MAC address - Physical

$\pi_1 \rightarrow R_1 \rightarrow \pi_2$

$R_1 \rightarrow \pi_3 \rightarrow R_2 \rightarrow \pi_1$

$R_2 \rightarrow \pi_1 \rightarrow \pi_2 \rightarrow R_2$



Qn: Consider the following set of processes with the following:

| Pid. | Priority | Burst | Arrival |
|------|----------|-------|---------|
| P1 | 40 | 20 | 0 |
| P2 | 30 | 25 | 25 |
| P3 | 30 | 25 | 30 |
| P4 | 35 | 15 | 60 |
| P5 | 5 | 10 | 100 |
| P6 | 10 | 10 | 105 |
| idle | 0 | | |

Here, higher number indicates higher priority.

Time quantum size is 10 units.

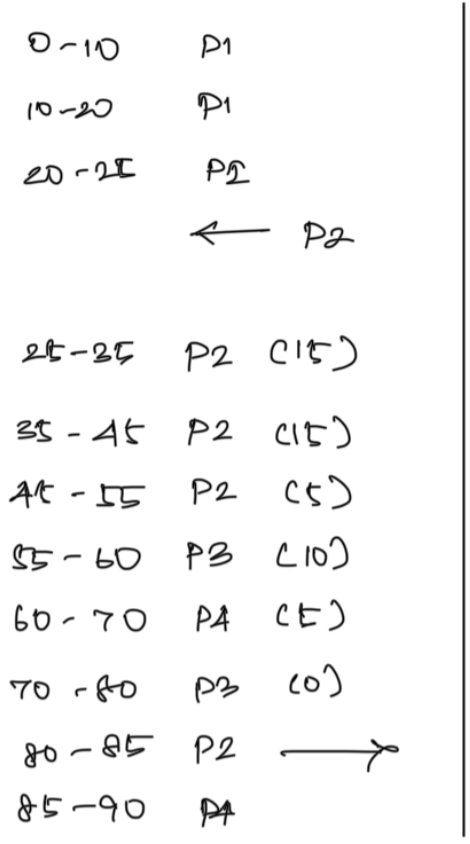
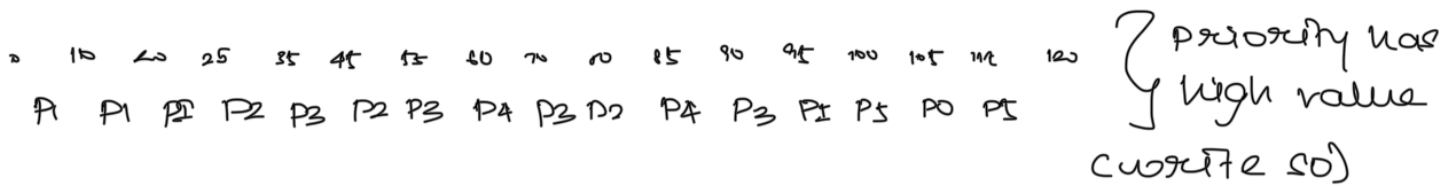
If a process is pre-empted, it is placed at the end of a queue.

Sketch gantt chart in RR

Find waiting and average time

Sketch the process state diagram and explain it

Progressive path.



Portions:

Apply: sys call and scheduling, process state

Deadlock detection:

- How often deadlock occurs
- How many threads are involved.
- How many resources are in shortage.

Recovery:

- Abort all deadlock processes.
- Abort one process at a time till deadlock cycle RR loop is eliminated.

- ↳ Priority
- ↳ Level of completion
- ↳ No. of dependent processes.

- Resource preemption: select a victim, roll back and starvation.

Qn: which one of the six will lead to deadlock?

When it is not deadlocked, outline the sequence

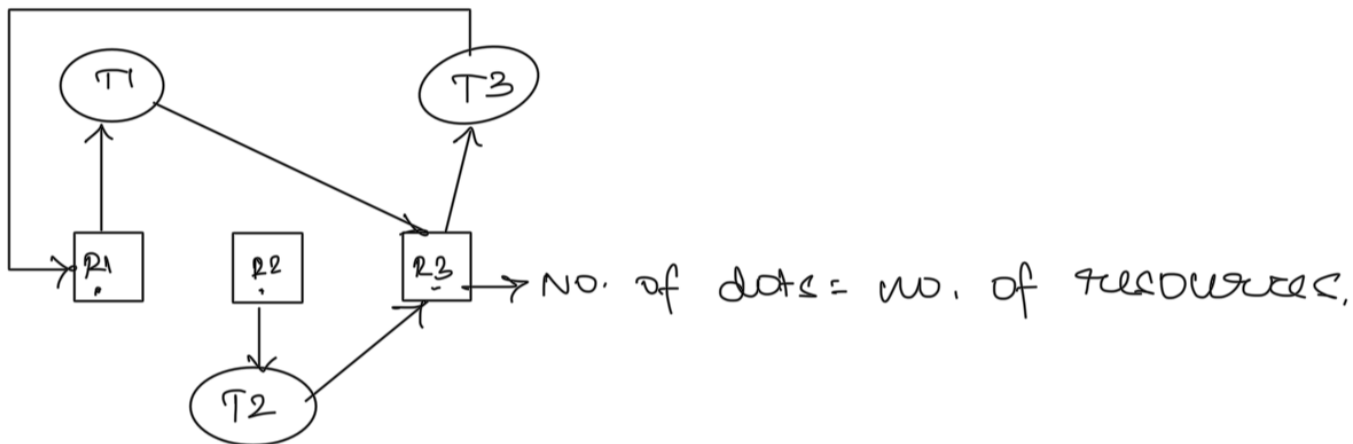
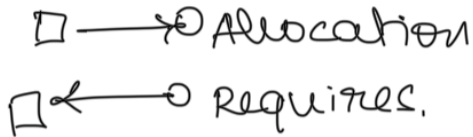
of thread execution.

Also, write the resource allocation and wait for execution of all six types.

Bankers' Algorithm .x.

Wait-for graph: with arrows.

Resource allocation: without arrows.



T1 and T3 are in a circular dependency
 T2 also depends on P3 ⇒ DEADLOCK.

PYQs:

(i) FCFS:

Burst time = Execution time.

| | | | | |
|----|----|----|----|----|
| P1 | P2 | P3 | P4 | P5 |
| 0 | 8 | 12 | 21 | 26 |

| P | B/E | A | S | W = S - A | TAT = W + B |
|---|-----|---|----|-----------|-------------|
| 1 | 8 | 0 | 0 | 0 | 8 |
| 2 | 4 | 1 | 8 | 7 | 11 |
| 3 | 9 | 2 | 12 | 10 | 19 |
| 4 | 5 | 3 | 21 | 18 | 23 |
| 5 | 3 | 4 | 26 | 22 | 25 |

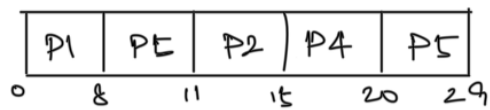
∴ Avg. wait time = 11.4 ms

Avg. TAT = 18 ms.

(ii) SJF

| P | B | A | S | W = S - A | TAT = B + W |
|---|---|---|---|-----------|-------------|
| 1 | 8 | 0 | 0 | 0 | 8 |

| | | | | | |
|---|---|---|----|----|----|
| 5 | 3 | 4 | 8 | 4 | 8 |
| 2 | 4 | 1 | 11 | 10 | 14 |
| 4 | 5 | 3 | 15 | 12 | 17 |
| 3 | 9 | 2 | 20 | 18 | 27 |



∴ Avg. wait time = 8.8 ms

Avg. TAT = 4.6 ms

Various ways in which synchronisation problem can be solved,

Consider the following process scenario:

| Process | (A, B, C, D) Allocation | Max ^m Demand | (M-A) Need | Common Available |
|---------|----------------------------|----------------------------|---------------|---------------------|
| P1 | 3 0 1 4 | 5 1 7 | 2 1 0 3 | 0 3 0 1 |
| ✓ P2 | 2 2 1 0 | 3 2 1 1 | 1 0 0 1 | |
| ✓ P3 | 3 1 2 1 | 3 3 2 1 | 0 2 0 0 | |
| ✓ P4 | 0 5 1 0 | 4 6 1 2 | 4 1 0 2 | |
| P5 | 4 2 1 2 | 6 3 2 5 | 2 1 1 3 | |

Banker's algorithm for the following: (10M)

(i) Available: 0, 3, 0, 1

(ii) Available: 1, 0, 0, 2

Now, we shall plot Banker's Algorithm and safe sequence.

Here,

work =

| | | | |
|---|---|---|---|
| 0 | 3 | 0 | 1 |
|---|---|---|---|

finish [i] = false $\forall i = 1 \dots 5$

1. finish [i] = false ✓

P3 → P2 → P4

Need [i] =

| | | | |
|---|---|---|---|
| 2 | 1 | 0 | 3 |
|---|---|---|---|

| |
|--------------------|
| finish [i] = false |
| Need [i] ≤ work |

work [i] =

| | | | |
|---|---|---|---|
| 0 | 3 | 0 | 1 |
|---|---|---|---|

 ✗

2.

finish [i] = false ✓

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 1 & 0 & 0 & 1 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 0 & 2 & 0 & 1 \\ \hline \end{array} \quad \times$$

$$3. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 0 & 2 & 0 & 0 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 0 & 3 & 0 & 1 \\ \hline \end{array} \quad \checkmark$$

$$\text{finish}[i] = \text{true}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 4 & 2 & 2 \\ \hline \end{array} \quad (\text{work} = \text{work} + \text{allocation})$$

$$4. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 4 & 1 & 0 & 2 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 4 & 2 & 2 \\ \hline \end{array} \quad \times$$

$$5. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 2 & 1 & 1 & 3 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 4 & 2 & 2 \\ \hline \end{array} \quad \times$$

$$1. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 2 & 1 & 0 & 3 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 4 & 2 & 2 \\ \hline \end{array} \quad \times$$

$$2. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 2 & 1 & 1 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 3 & 4 & 2 & 2 \\ \hline \end{array} \quad \checkmark$$

$$\text{finish}[i] = \text{true}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 5 & 6 & 3 & 2 \\ \hline \end{array}$$

$$4. \text{ finish}[i] = \text{false} \checkmark$$

$$\text{need}[i] = \begin{array}{|c|c|c|c|} \hline 4 & 1 & 0 & 2 \\ \hline \end{array}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 5 & 6 & 3 & 2 \\ \hline \end{array}$$

$$\text{finish}[i] = \text{true}$$

$$\text{work}[i] = \begin{array}{|c|c|c|c|} \hline 5 & 11 & 4 & 2 \\ \hline \end{array}$$

5. finish [i] = false ✓

Need [i] =

| | | | |
|---|---|---|---|
| 2 | 1 | 1 | 3 |
|---|---|---|---|

work [i] =

| | | | |
|---|----|---|---|
| 5 | 11 | 4 | 2 |
|---|----|---|---|

 ✗

1. finish [i] = false ✓

Need [i] =

| | | | |
|---|---|---|---|
| 2 | 1 | 0 | 3 |
|---|---|---|---|

work [i] =

| | | | |
|---|----|---|---|
| 5 | 11 | 4 | 2 |
|---|----|---|---|

 ✗

∴ Ans.: P3 → P2 → P4 → Deadlock
(P1, P5 are unsafe).

(ii) Given,

work =

| | | | |
|---|---|---|---|
| 1 | 0 | 0 | 2 |
|---|---|---|---|

finish [i] = false $\forall i=1, \dots, 5$

| Need |
|---------|
| 2 1 0 3 |
| 1 0 0 1 |
| 0 2 0 0 |
| 4 1 0 2 |
| 2 1 1 3 |

1.

finish [i] = false ✓

Need [i] =

| | | | |
|---|---|---|---|
| 2 | 1 | 0 | 3 |
|---|---|---|---|

work [i] =

| | | | |
|---|---|---|---|
| 1 | 0 | 0 | 2 |
|---|---|---|---|

 ✗

P2 → P3 → P4 → P5
→ P1

2.

finish [i] = false ✓

Need [i] =

| | | | |
|---|---|---|---|
| 1 | 0 | 0 | 1 |
|---|---|---|---|

work [i] =

| | | | |
|---|---|---|---|
| 1 | 0 | 0 | 2 |
|---|---|---|---|

 ✓

finish [i] = true

work [i] =

| | | | |
|---|---|---|---|
| 3 | 2 | 1 | 2 |
|---|---|---|---|

3.

finish [i] = false ✓

Need [i] =

| | | | |
|---|---|---|---|
| 0 | 2 | 0 | 0 |
|---|---|---|---|

work [i] =

| | | | |
|---|---|---|---|
| 3 | 2 | 1 | 2 |
|---|---|---|---|

 ✓

finish [i] = true

work [i] =

| | | | |
|---|---|---|---|
| 6 | 3 | 3 | 3 |
|---|---|---|---|

4.

finish [i] = false ✓

Need [i] = $\begin{bmatrix} 4 & 1 & 0 & 2 \end{bmatrix}$
 Work [i] = $\begin{bmatrix} 6 & 3 & 2 & 3 \end{bmatrix}$ ✓

finish [i] = true
 work [i] = $\begin{bmatrix} 6 & 8 & 4 & 3 \end{bmatrix}$

5.

finish [i] = false ✓
 Need [i] = $\begin{bmatrix} 2 & 1 & 1 & 3 \end{bmatrix}$
 work [i] = $\begin{bmatrix} 6 & 8 & 4 & 3 \end{bmatrix}$

finish [i] = true
 work [i] = $\begin{bmatrix} 10 & 10 & 5 & 5 \end{bmatrix}$

1.

finish [i] = false
 Need [i] = $\begin{bmatrix} 2 & 1 & 0 & 3 \end{bmatrix}$
 work [i] = $\begin{bmatrix} 10 & 10 & 5 & 5 \end{bmatrix}$

∴ Ans: P2 → P3 → P4 → P5 → P1

Request grant:

(i) Request_i ≤ Need_i

(ii) Request_i ≤ Available_i

Process:

Available = Available - Request_i
 Allocation_i = Allocation + Request_i
 Need_i = Need_i - Request_i

| Process | Allocation | Max | Avail |
|----------------|------------|---------|--------------------|
| P ₁ | 2 0 0 1 | 4 2 1 2 | 2 3 2 1 |
| P ₂ | 3 1 2 1 | 5 2 5 2 | |
| P ₃ | 2 1 0 3 | 2 3 1 6 | |
| P ₄ | 1 3 1 2 | 1 4 2 4 | |
| P ₅ | 1 4 3 2 | 3 6 6 5 | |

Resource-Request: ID Array

Request < Need
 Request < Available } if allocation.

↓ T = satisfied

$Available = Available - Request_i$
 $Allocation_i = Allocation + Request_i$
 $Need_i = Need_i - Request_i$

| Process | Allocation | | | | Max | | | | Need | | | | Avail. | | | |
|---------|------------|---|---|---|-----|---|---|---|------|---|---|---|--------|---|---|---|
| | A | B | C | D | A | B | C | D | A | B | C | D | A | B | C | D |
| P1 | 2 | 0 | 0 | 1 | 4 | 2 | 1 | 2 | 2 | 2 | 1 | 1 | 3 | 3 | 2 | 1 |
| P2 | 3 | 1 | 2 | 1 | 5 | 2 | 5 | 2 | 2 | 1 | 3 | 1 | | | | |
| P3 | 2 | 1 | 0 | 3 | 2 | 3 | 1 | 6 | 0 | 2 | 1 | 3 | | | | |
| P4 | 1 | 3 | 1 | 2 | 1 | 4 | 2 | 4 | 0 | 1 | 1 | 2 | | | | |
| P5 | 1 | 4 | 3 | 2 | 3 | 6 | 6 | 5 | 2 | 2 | 3 | 3 | | | | |

finish [i] = false $\forall p = 1, 2, \dots, 5$

work 3 3 2 1

① Need \leq work: work = work + allocation

finish [i] = true

work: $\begin{array}{cccc} 3 & 3 & 2 & 1 \\ 2 & 0 & 0 & 1 \\ \hline 5 & 3 & 2 & 2 \end{array}$

P1 \rightarrow P4 \rightarrow P5 \rightarrow P2 \rightarrow P3

② Not satisfied

③ NO

④ finish [4] = true

work: $\begin{array}{cccc} 5 & 3 & 2 & 2 \\ 1 & 3 & 1 & 2 \\ \hline 6 & 6 & 3 & 4 \end{array}$

⑤ work: $\begin{array}{cccc} 6 & 6 & 3 & 4 \\ 1 & 4 & 3 & 2 \\ \hline 7 & 10 & 6 & 6 \end{array}$

⑥ work: $\begin{array}{cccc} 7 & 10 & 6 & 6 \\ 3 & 1 & 2 & 1 \\ \hline 10 & 11 & 8 & 7 \end{array}$

⑦ work: $\begin{array}{cccc} 10 & 11 & 8 & 7 \\ 2 & 1 & 0 & 3 \\ \hline \end{array}$

Request-checking:

P1: 1 1 0 0

| Process | Allocation | Max | Need | Avail |
|---------|------------|---------|---------|-----------|
| | A B C D | A B C D | A B C D | |
| P1 | 2 1 0 1 | 4 2 1 2 | 1 1 1 1 | 2 2 2 1 |
| P2 | 3 1 2 1 | 5 2 5 2 | 2 1 3 1 | - 1 1 0 0 |
| P3 | 2 1 0 3 | 2 3 1 6 | 0 2 1 3 | 2 2 2 1 |
| P4 | 1 3 1 2 | 1 4 2 4 | 0 1 1 2 | |
| P5 | 1 4 3 2 | 3 6 6 5 | 2 2 3 3 | |

Safe sequence:

① Work: 2 2 2 1
Need ≤ 1

P1 → P4 → P5 → P2 → P3

$$\begin{array}{r} \text{Work} = 2 \ 2 \ 2 \ 1 \\ \quad \quad 3 \ 1 \ 0 \ 1 \\ \hline \quad \quad 5 \ 3 \ 2 \ 2 \end{array}$$

② NO

③ NO

$$\begin{array}{r} \text{④ Work} = 5 \ 3 \ 2 \ 2 \\ \quad \quad 1 \ 3 \ 1 \ 2 \\ \hline \quad \quad 6 \ 6 \ 3 \ 4 \end{array}$$

$$\begin{array}{r} \text{⑤ Work} = 6 \ 6 \ 3 \ 4 \\ \quad \quad 1 \ 4 \ 3 \ 2 \\ \hline \quad \quad 7 \ 10 \ 6 \ 6 \end{array}$$

$$\begin{array}{r} \text{⑥ Work:} 7 \ 10 \ 6 \ 6 \\ \quad \quad 3 \ 1 \ 2 \ 1 \\ \hline \quad \quad 10 \ 11 \ 8 \ 7 \end{array}$$

$$\begin{array}{r} \text{⑦ Work:} 10 \ 11 \ 8 \ 7 \\ \quad \quad 2 \ 1 \ 0 \ 3 \\ \hline \quad \quad 12 \ 12 \ 8 \ 10 \end{array}$$

(ii) Not possible

FIFO, Optimal, LRU

(i) FIFO:

| | | | | | | | | | | |
|----|---|---|---|-----|---|-----|---|---|---|-----|
| | | 3 | 3 | 3 | 3 | 3 | A | A | A | A |
| | 2 | 2 | 2 | 2 | 5 | 5 | 5 | 5 | 7 | 7 |
| 7 | 7 | 7 | 1 | 1 | 1 | 1 | 1 | 6 | 6 | 6 |
| M | M | M | M | Hit | M | Hit | M | M | M | Hit |
| 10 | 1 | 1 | 4 | 4 | 4 | 3 | 3 | 3 | 4 | 4 |
| 7 | 7 | 5 | 5 | 5 | 2 | 2 | 2 | 1 | 1 | 0 |
| 6 | 0 | 0 | 0 | 6 | 6 | 6 | 0 | 0 | 0 | 6 |
| M | M | M | M | M | M | M | M | M | M | M |
| 0 | | | | | | | | | | |
| 6 | | | | | | | | | | |
| M | | | | | | | | | | |

Page fault: 21

(ii) LRU:

| | | | | | | | | | | |
|---|---|---|---|-----|---|---|---|---|---|-----|
| | | 3 | 3 | 3 | 5 | 5 | 5 | 6 | 6 | 6 |
| | 2 | 2 | 2 | 2 | 2 | 2 | 4 | 4 | 4 | 4 |
| 7 | 7 | 7 | 1 | 1 | 1 | 3 | 3 | 3 | 7 | 7 |
| M | M | M | M | Hit | M | M | M | M | M | Hit |
| 6 | 0 | 0 | 0 | 6 | 6 | 6 | 0 | 0 | 0 | |
| 1 | 1 | 1 | 4 | 4 | 4 | 3 | 3 | 3 | 4 | |
| 7 | 7 | 5 | 5 | 5 | 2 | 2 | 2 | 1 | 1 | |
| M | M | M | M | M | M | M | M | M | M | |
| 6 | 6 | 6 | | | | | | | | |
| A | A | 1 | | | | | | | | |
| 1 | 0 | 0 | | | | | | | | |
| M | M | M | | | | | | | | |

Page fault: 22

(iii) Optimal:

| | | | | | | | | | | |
|-----|---|-----|---|-----|---|-----|-----|-----|---|-----|
| | | 3 | 3 | 3 | 3 | 3 | A | 6 | 7 | 7 |
| | 2 | 2 | 2 | 2 | 5 | 5 | 5 | 5 | 5 | 5 |
| 7 | 7 | 7 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| M | M | M | M | Hit | M | Hit | M | M | M | Hit |
| 7 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 |
| 5 | 5 | 5 | 4 | 6 | 2 | 3 | 7 | 3 | 4 | 6 |
| 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| Hit | M | Hit | M | M | M | M | Hit | Hit | M | M |

24 - 9 = 15 Page faults.

Memory ↓

$$\text{Time} = (\text{Hit ratio} \times \text{Hit time}) + (\text{Miss ratio} \times \text{Miss time})$$

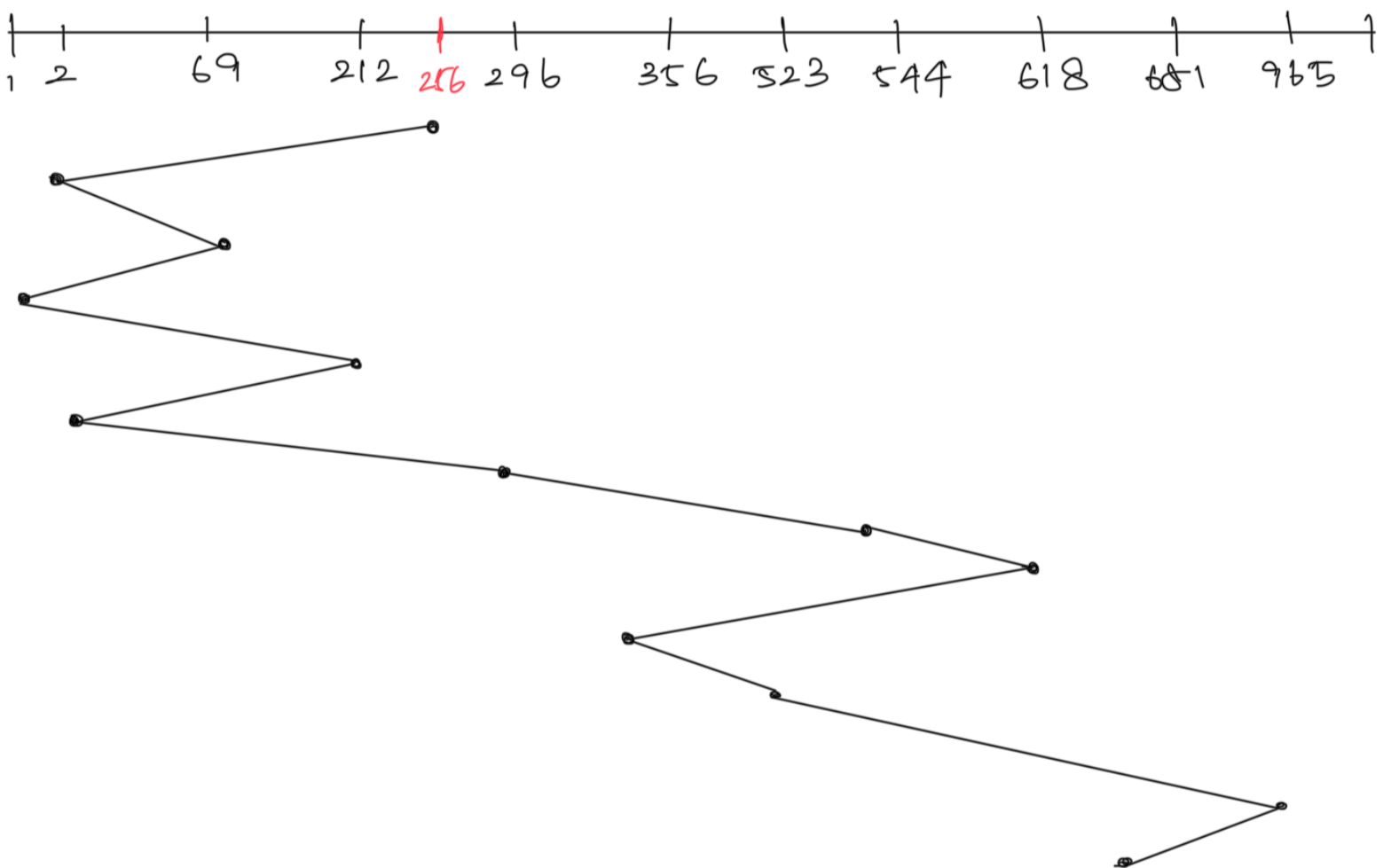
52 102

TLB: Translation Lookaside Buffer

Total overhead movement → calc. first

Disc scheduling:

① FIFO

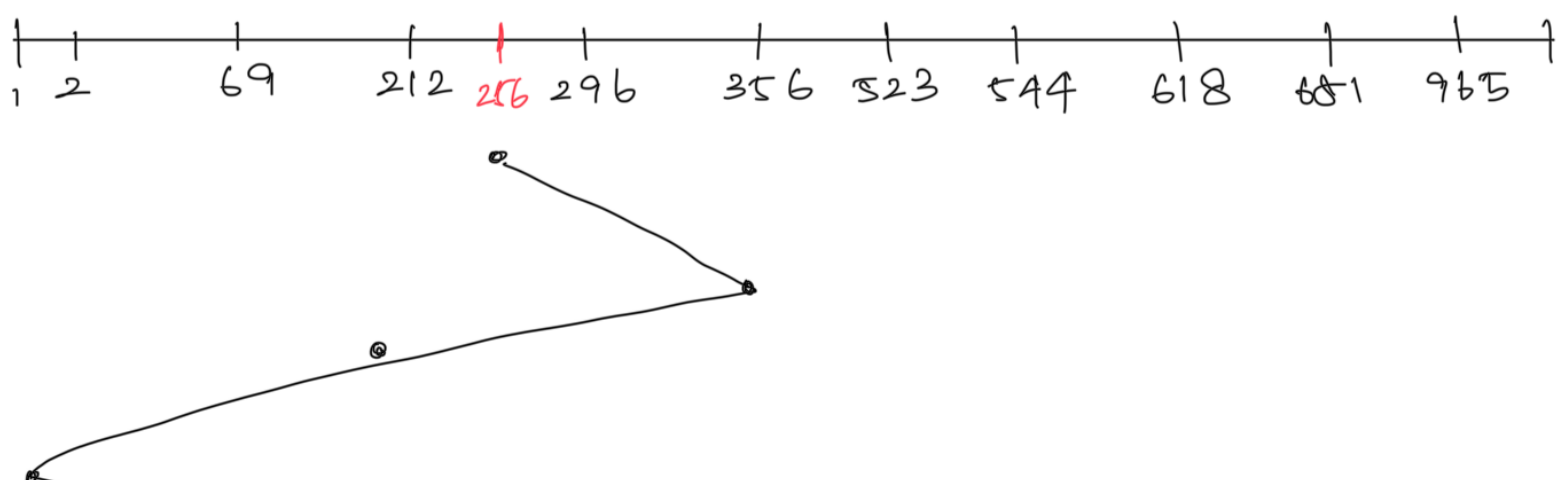


Total overhead movement:

$$254 + 67 + 68 + 211 + 210 + 294 + 248 + 74 + 262 + 167 + 442 + 284$$

$$= 2581$$

② SSTF



\therefore Answer: 1419

③ SCAN:

$$(999 - 256) + (999 - 1) = 1741$$

④ CSCAN:

$$(999 - 256) + (999 - 0) + (212 - 0) = 1954$$

⑤ C LOOK:

$$(965 - 256) + (965 - 1) + (212 - 1) = 1884$$

⑥ LOOK:

$$(965 - 256) + (965 - 1) = 1673$$

Logical address \rightarrow pg no., offset

\downarrow main memory

Phy. address. \rightarrow frame no., offset